Amy Status 8/5/84

- Establish weekly meetings with Chip Group (ASG) - see attached status report.  
- Debug 2nd Amy Broadband - prep. for test cards (focus on sensor test development)
- Compare MOS (Coats) and BBRD (TTL) schematics, input choices to ASG
- Finish Rev. C Amy Spec; done except for some 6/16 timing diagrams
- Find analyzer to do proper MOS simulations of the following:
  1) Ramps
  2) Roms
  3) Clock circuitry
  4) Precharge circuit
  5) Input ETIF & switcher
- Get Amy test program staffed; in progress
- Help define scope & instrument tube data formats (including interrupt)
- Finish driver (6.33) flow charts; in progress
- Help define menu input 541 & 114; in progress
Tasks for the week of 7/30/84 to 8/3/84

ENGINERS

San Calpa
   Encl - PHASE RAM
   Start - NOISE RAM

Chuck Hung
   NOT DONE

John Mahoney
   2 done
   3 done
   3 done

Darryl Eng

TASKS TO BE DONE

Work on clock buffer-precharge circuit simulations and will be using results to simulate RAM circuit.

Work on Daisy to update logic diagram on the ROM portions so that the ROM coding can be implemented for NCC at later stage.

Work on ROM core programming for layout, check on clock/RAM/ROM logic/circuit. Aid layout with edits from first continuity check. Preparing plots for second continuity check and labeling text on pads for ERC and running NCC at later stage.

Calma - 2 Roms programmed
3 Roms being worked on
Inter Office Memo

To: Sam Nicolino

From: Bob Hemming

Subject: AMY Progress Report

Date: 7/30/84

Goals for week ending on August 7, 1984.

NCC conversion for AMY:

1) ROM coding will be completed on August 3, 1984 by John Mahoney.

2) RAM decoder and RAM core cell conversion will be completed on August 5, 1984 by Delwin Persson.

3) Update logic on Daisy from finalized logic in 2 days after receiving signed-off logic from Sam Nicolino. Logic update will be done by Bob Hemming.

Silos test vector generation:

1) Sin and exp. ROM test vector done by August 3, 1984 by Bob Hemming. Final pattern will be generated from net list exacted from final Daisy logic diagrams.

cc: Roland Ernst
    Carl Nielsen

BH/bjk
CURRENTLY FOUR PEOPLE IN THE LAYOUT DESIGN ARE WORKING FULL TIME TO ACCOMPLISH THE EDITS AND CHANGES THAT WERE FOUND IN THE FIRST ATTENDANCE SHEET. THE SCHEDULE THRU 9/10/84 IS AS FOLLOWS:

<table>
<thead>
<tr>
<th>Task</th>
<th>Estimated Date</th>
<th>Personnel</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start Coloring Second Continuity Plots</td>
<td>8/1/84</td>
<td>Dave Eastman</td>
</tr>
<tr>
<td>Finish First Continuity Edits</td>
<td>8/2/84</td>
<td>Adrian Davis, Terry Scott, Steve Kimura, Theresa Adams</td>
</tr>
<tr>
<td>Start DRC Run</td>
<td>8/2/84</td>
<td>Adrian Davis</td>
</tr>
<tr>
<td>Start Second Continuity Check (2A)</td>
<td>8/3/84</td>
<td>Steve Kimura</td>
</tr>
<tr>
<td>Finish Coloring Second Continuity Plots</td>
<td>8/3/84</td>
<td>Dave Eastman, Terry Scott</td>
</tr>
<tr>
<td>Start DRC Edits</td>
<td>8/3/84</td>
<td>Adrian Davis, Terry Scott, Theresa Adams</td>
</tr>
<tr>
<td>Start Second Continuity Edits (2A)</td>
<td>8/7/84</td>
<td>Adrian Davis, Terry Scott, Theresa Adams</td>
</tr>
<tr>
<td>Start Second Continuity Check (2B)</td>
<td>8/9/84</td>
<td>Terry Scott, Theresa Adams</td>
</tr>
</tbody>
</table>

1) Am
2) NCR
To: Sam Nicolino  
From: Lyle Sopp  
Subject: AMY Data Sheet  
Date: 7/30/84

The AMY data sheet was delivered to you about 2 weeks ago for your correction. I have yet to receive your updates. The Rev. A version (current), has been in the update mode for over 3 months. George Wang finished one set of revisions on 6/1/84. I plan no action on updating this spec until you have sent to me the marked up version which I gave you. After I receive your revised copy, I will then give you an estimate on when the spec will be completed.

CC: Carl Nielsen